



GROUP 2500

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Vora Art Unit: 2512

Examiner: GIORDANA

3 # 12/11/99 al Brow

Serial No. 08/654,760

Filed:

5/29/96

For VERTICALLY INTEGRATED FLASH EEPROM FOR GREATER DENSITY AND LOWER COST

VIA FACSIMILE 703 308 7722

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

Morgan Hill, California November 24, 1997

Attn: Examiner Glordana

SUPPLEMENTAL AMENDMENT

Dear Sir:

In response to the Office Action mailed , please amend the above identified dase as follows.

IN THE DRAWINGS

Enclosed is a markup of Figure 33 for approval by the Examiner.

Remarks

The undersigned noticed, after filing the amendment in response to the Office action mailed 7/15/97 on 11/15/97, that one of the drawings showing a cross section of the finished device has an error. Specifically, the error is that bit line 122 is shown as shorting the P region acting as the transistor channel region to the N regions acting as

V&F-001 Amend 11/97

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the s urce and drain. This short occurs becaus the P-type channel region and the N-type source regin are shown as coming to the surface which they should not do at least in the vicinity on the surface where the bit line will short them together. The undersigned believes that those skilled in the art will appreciate this fact, and, since Figure 5 shows a correct cross section of the completed device, no new matter issue is raised by this change.

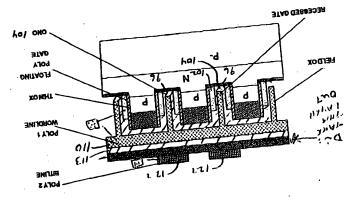
All claims are believed to be in condition for allowance, and favorable action is ernestly solicited.

Dated: November 24, 1997

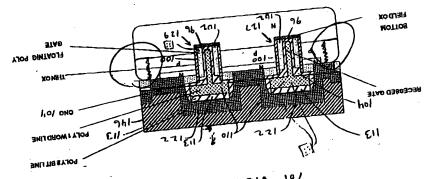
Respectfully submitted,

Ronald Craig Fish Reg. No. 28,843 Tel 408 778 3624 FAX 408 776 0428

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F16.33



HELDOX

821

N+ DHAIN REGIONS

Statute Stop

POLY I WORD LINE